# Efficient Longest Common Subsequence Computation using Bulk-Synchronous Parallelism

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# Outline

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- Motivation
- The BSP Model

#### Problem Definition and Algorithms

- The Standard Algorithm
- Standard Algorithm
- Bit-Parallel Algorithm
- The Parallel Algorithm

#### 3 Experiments

- Experiment Setup
- Predictions
- Speedup

# **Motivation**

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Computing the (Length of the) Longest Common Subsequence is representative of a class of dynamic programming algorithms for string comparison. Hence, we want to

- Start with a fast sequential algorithm.
- Examine the suitability of BSP as a programming model for such problems.
- Compare different BSP libraries on different systems.
- Examine performance predictability.

# The BSP Computer

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- *p* identical processor/memory pairs (computing nodes), computation speed *f*
- Arbitrary interconnection network, latency l, bandwidth g



### **BSP** Programs

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- Programs are SPMD
- Execution takes place in *supersteps* 
  - Communication may be delayed until the end of the superstep
  - Separates communication and computation
- Cost/Running time Formula :

$$T = f \cdot W + g \cdot H + l \cdot S$$

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# 'BSP-style' programming using a conventional communications library (MPI/Cray shmem/...)

- Barrier synchronizations for creating superstep structure
- Message passing or remote memory access for communication

Using a specialized library (The Oxford BSP Toolset/PUB/CGMlib/...)

- Optimized barrier synchronization functions and message routing
- Higher level of abstraction / nicer looking code.

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## **Problem Definition**

#### Definition (Input data)

Let  $X = x_1 x_2 \dots x_m$  and  $Y = y_1 y_2 \dots y_n$  be two strings on an alphabet  $\Sigma$  of constant size.

#### Definition (Subsequences)

A *Subsequence U* of *X*: *U* can be obtained by deleting zero or more elements from *X*.

#### Definition (Longest Common Subsequences)

A *LCS* (X, Y) is any string which is subsequence of both X and Y and has maximum possible length. Length of these sequences: *LLCS* (X, Y).

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## The Dynamic Programming Matrix

Definition (Matrix *L*<sub>0...m</sub>, 0...n)

$$L_{i,j} = \begin{cases} 0 & \text{if } i = 0 \text{ or } j = 0, \\ L_{i-1,j-1} + 1 & \text{if } x_i = y_j, \\ max(L_{i-1,j}, L_{i,j-1}) & \text{if } x_i \neq y_j . \end{cases}$$

Theorem (Hirschberg, '75)

 $L_{i,j} = LLCS(x_1x_2...x_i, y_1y_2...y_j)$ . The values in this matrix can be computed in O(mn) time and space.

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# **Bit-Parallel Algorithm**

Bit-parallel computation has same asymptotic complexity but processes  $\omega$  entries of *L* in parallel ( $\omega$  : machine word size).

Example (this leads to substantial speedup)



## How does it work?

#### • $\Delta L(i, j) = L(i, j) - L(i - 1, j) \in \{0, 1\}$

 ΔL(i, j) is computed columnwise using machine-word parallel operations :

$$\sim \Delta L(i, j) \leftarrow (\sim \Delta L(i, j) + (\sim \Delta L(i, j - 1) \text{ and } M(x_j)))$$
  
or (~  $\Delta L(i, j - 1) \text{ and} (\sim M(x_j)))$ 

M maps characters to bit strings of length m,

$$M(\sigma \in \Sigma)_i = 1 \Leftrightarrow x_i = \sigma$$

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# The Parallel Algorithm

- Matrix L is partitioned into a grid of rectangular blocks of size (m/G) × (n/G) (G : grid size)
- Blocks in a wavefront can be processed in parallel
- Assumptions:
  - Strings of equal length m = n
  - Ratio  $\alpha = \frac{G}{p}$  is an integer



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### Parallel Cost Model

- When G > p, there can be multiple stages for one block-wavefront
- Running time

$$T(\alpha) = f \cdot (p\alpha(\alpha+1) - \alpha) \cdot \left\lceil \frac{n}{\alpha p} \right\rceil^{2} + g \cdot \alpha(\alpha p - 1) \left\lceil \frac{n}{\alpha p} \right\rceil + l \cdot (2\alpha p - 1) \cdot \alpha$$



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## Experiments: Systems Used

- aracari: IBM cluster, 2-way SMP Pentium3 1.4 GHz nodes (Myrinet 2000)
- **argus:** Linux cluster, 2-way SMP Pentium4 Xeon 2.6 GHz nodes (100Mbit Ethernet)
- **skua:** SGI Altix shared memory machine, Itanium-2 1.6 GHz processors

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## Experimental values of f and f'

#### Simple Algorithm (f)

skua	0.008 ns/op	130 M op/s
argus	0.016 ns/op	61 M op/s
aracari	0.012 ns/op	86 M op/s

#### **Bit-Parallel Algorithm** (f')

skua	0.00022 ns/op	4.5 G op/s
argus	0.00034 ns/op	2.9 G op/s
aracari	0.00055 ns/op	1.8 G op/s

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#### Prediction Results Good Results (LLCS) ... (e.g. aracari MPI, 32 processors)



## **Good Predictions**

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- ... on all distributed memory systems, using both bit-parallel and standard algorithm
- ... on the shared memory system only for larger problem sizes, and for the standard algorithm

# Prediction Results

Not so good ones... (Oxtool, skua, 32 processors)



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# What happened?

Cache size effects prevent prediction of computation time...

Sequential computation performance on **skua** 



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# Other Problems when Predicting Performance

- Setup costs only covered by parameter l
  - ⇒ difficult to measure
  - ⇒ Problems when communication size is small
- PUB has performance break-in when communication size reaches a certain value

 Busy communication network can create 'spikes'

# Speedup for the Bit-Parallel Version

- Speedup lower than for the standard version
- However, overall running times for same problem sizes are shorter
- Can only expect parallel speedup for larger problem sizes
- Latency is problematic, as computation times are low.

## **Result Summary**

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Shared memory (sku	<b>Oxtool</b> a)	PUB	MPI
LLCS (standard) LLCS (bit-parallel)	•••	•	••
	•••	••	•
	•••	•••	•

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## Summary

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- BSP algorithms are efficient for dynamic programming.
- Implementations benefit from a low latency implementation (Oxtool/PUB)
- Very good predictability

# Outlook

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#### **Technical improvements**

- Different modeling of bandwidth allows better predictions
- Using assembly can double bit-parallel performance
- Lower latency possible by using subgroup synchronization

#### Algorithmic improvements

- Extraction of LCS possible, using post processing step or other algorithm
- Implementation of all-substrings LLCS (which has many applications)
- Design and study of subquadratic algorithms

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